

Abstract of the Disclosure

The present invention relates to a semiconductor device and, more particularly, to a clock enable buffer to
5 output a clock enable signal which enables a clock buffer to produce an internal clock signal. The semiconductor device according to the present invention comprises: a clock buffer for receiving and buffering an external clock signal and then outputting an internal clock; a clock enable buffer for
10 comparing a reference voltage having a constant potential with a clock enable buffer signal and then enabling the clock buffer; and a clock enable signal latch circuit for enabling the clock enable buffer using the clock enable buffer signal after a power-up signal is inputted.